

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,530,877 B1
APPLICATION NO. : 09/517127
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INVENTOR(S) : Moore et al.

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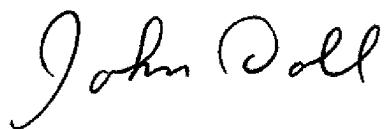
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 27, lines 8-9, in Claim 46, delete “semiconductorworkpiece” and insert
-- semiconductor workpiece --, therefor.

In column 27, line 18, in Claim 46, after “system” delete “is”.

Signed and Sealed this

Twenty-first Day of July, 2009



JOHN DOLL
Acting Director of the United States Patent and Trademark Office